

Press Release

Nuremberg, August 2018

BMBF Project 'SiC-DCBreaker' has been launched in the frame of Cluster Internationalization

Under the BMBF funding initiative for the internationalization of excellent clusters, future projects and comparable networks, the Power Electronics Cluster coordinates an international research cooperation between Cluster stakeholders and the Japanese Power Electronics Consortium NPERC-J headquartered in Tokyo. The SiC-DCBreaker project is about electronic circuit breaker for DC networks based on SiC technology.

In a two-year conception phase, the Power Electronics Cluster in ECPE e.V. headquartered in Nuremberg, together with selected Cluster stakeholders, has developed an internationalization concept for a research cooperation with Japan. This involves novel wide bandgap power semiconductors (silicon carbide, SiC and gallium nitride, GaN) and their system integration. In the following implementation phase of the cluster internationalization, two BMBF joint research projects, IsoGap and SiC-DCBreaker, were launched on 1 August 2018, with a term of three years and a total funding of three million euros.

In the SiC-DCBreaker project the cooperation partners on the German side, Infineon Technologies AG, Robert Bosch GmbH, Grass Power Electronics GmbH, ETA Elektrotechnische Apparate GmbH, the Institute IALB of Bremen University, the Fraunhofer Institute IISB and the Power Electronics Cluster in ECPE e.V. work on SiC-based circuit breakers for DC grids in high-voltage automotive on-board networks of electric vehicles as well as for applications in DC networks in buildings with PV supply. The Japanese sub-consortium includes university and industry partners from the NPERC-J consortium (New Generation Power Electronics and System Research Consortium Japan).

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